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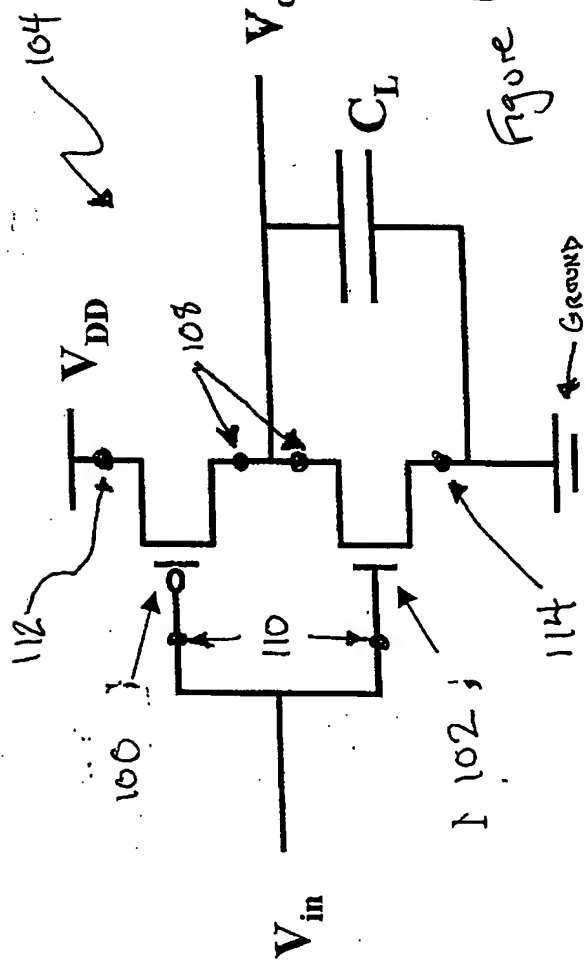


Figure 1a Prior Art

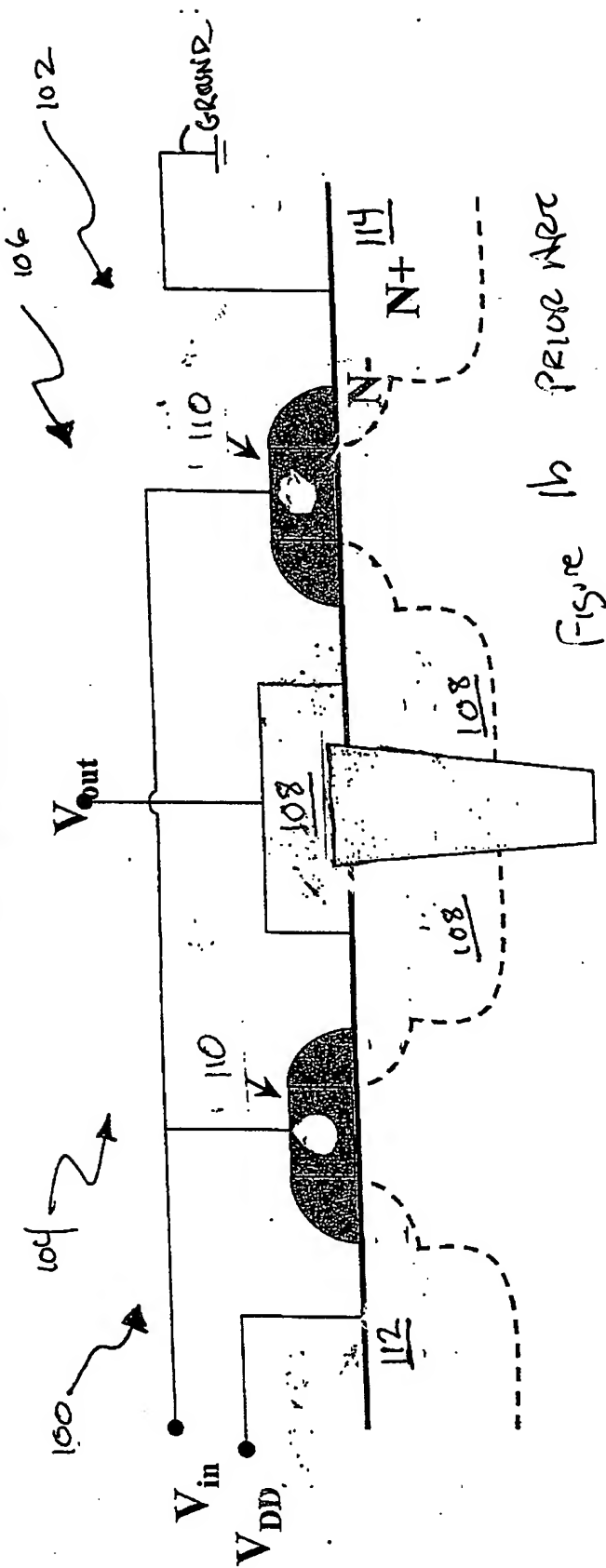


Figure 1b Prior Art

FIG. 1C)  
PRIOR ART

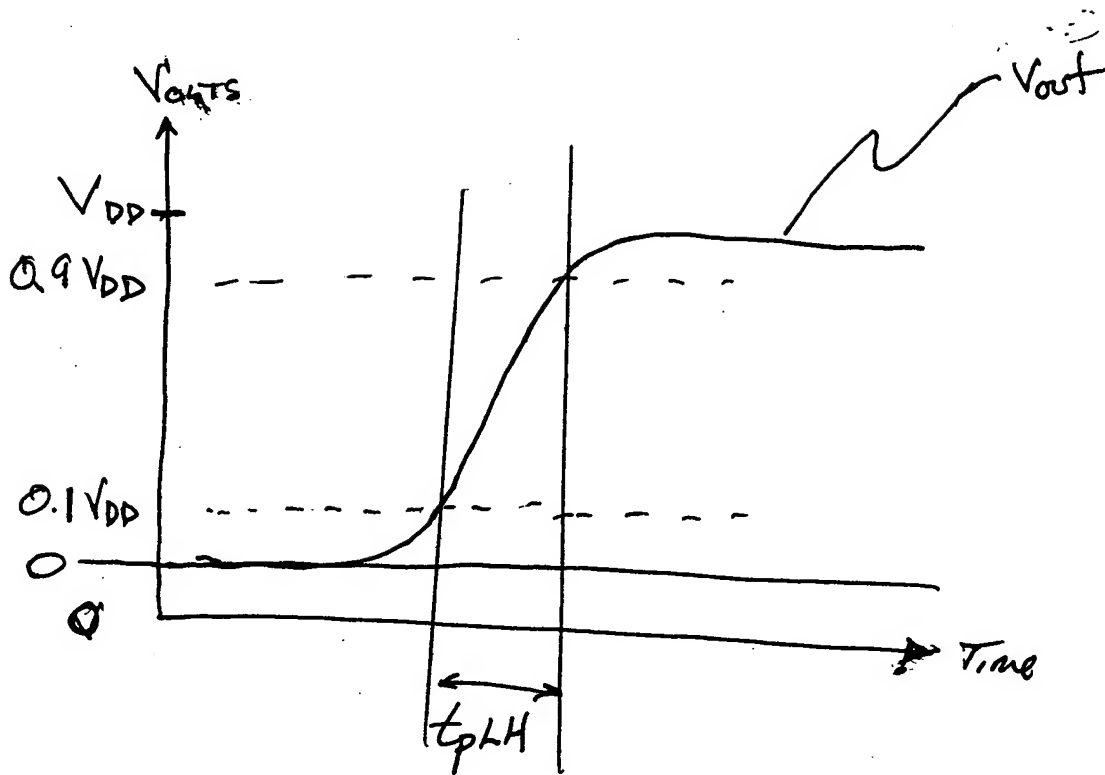


FIG 1D) PRIOR ART

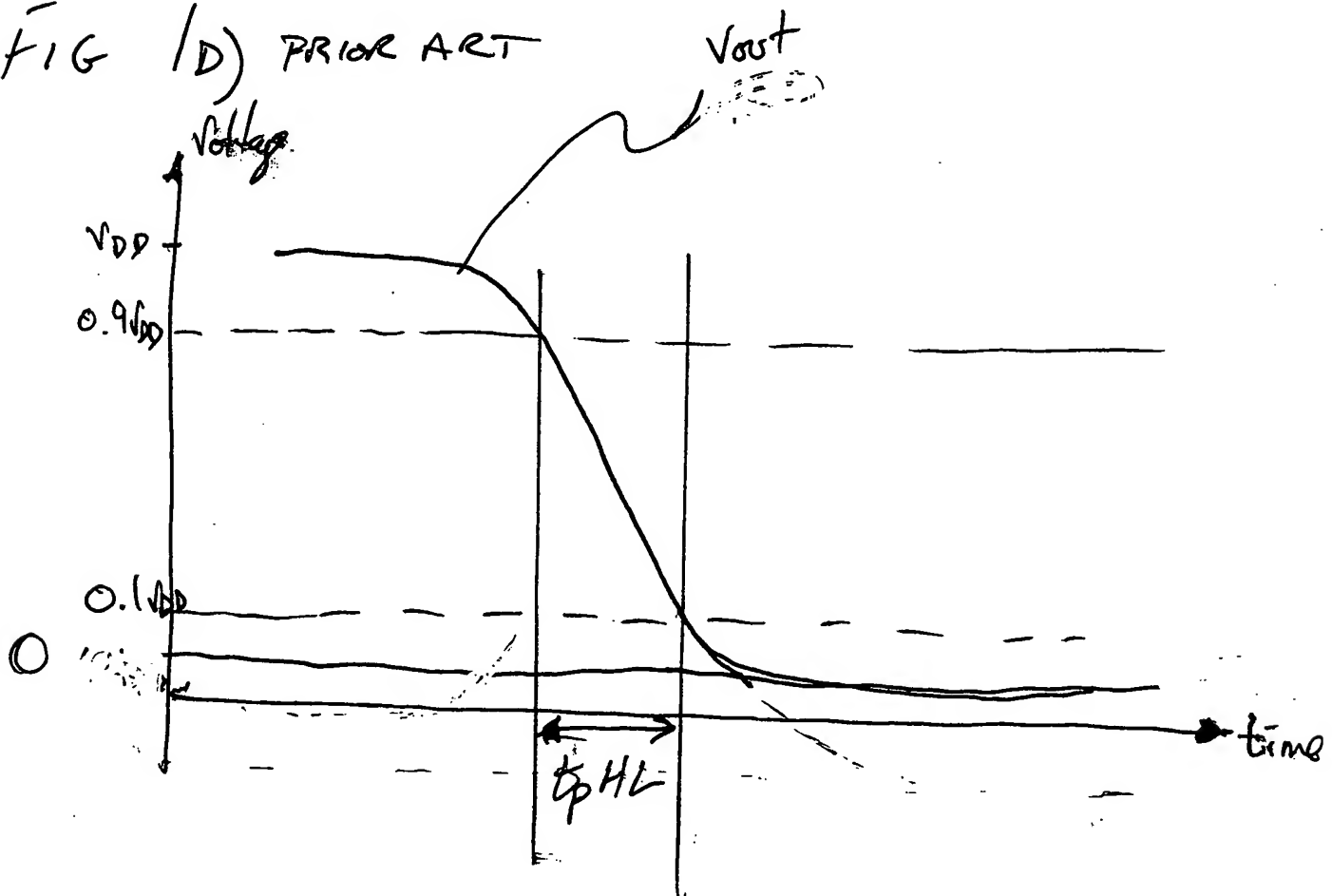


Fig 2. a)

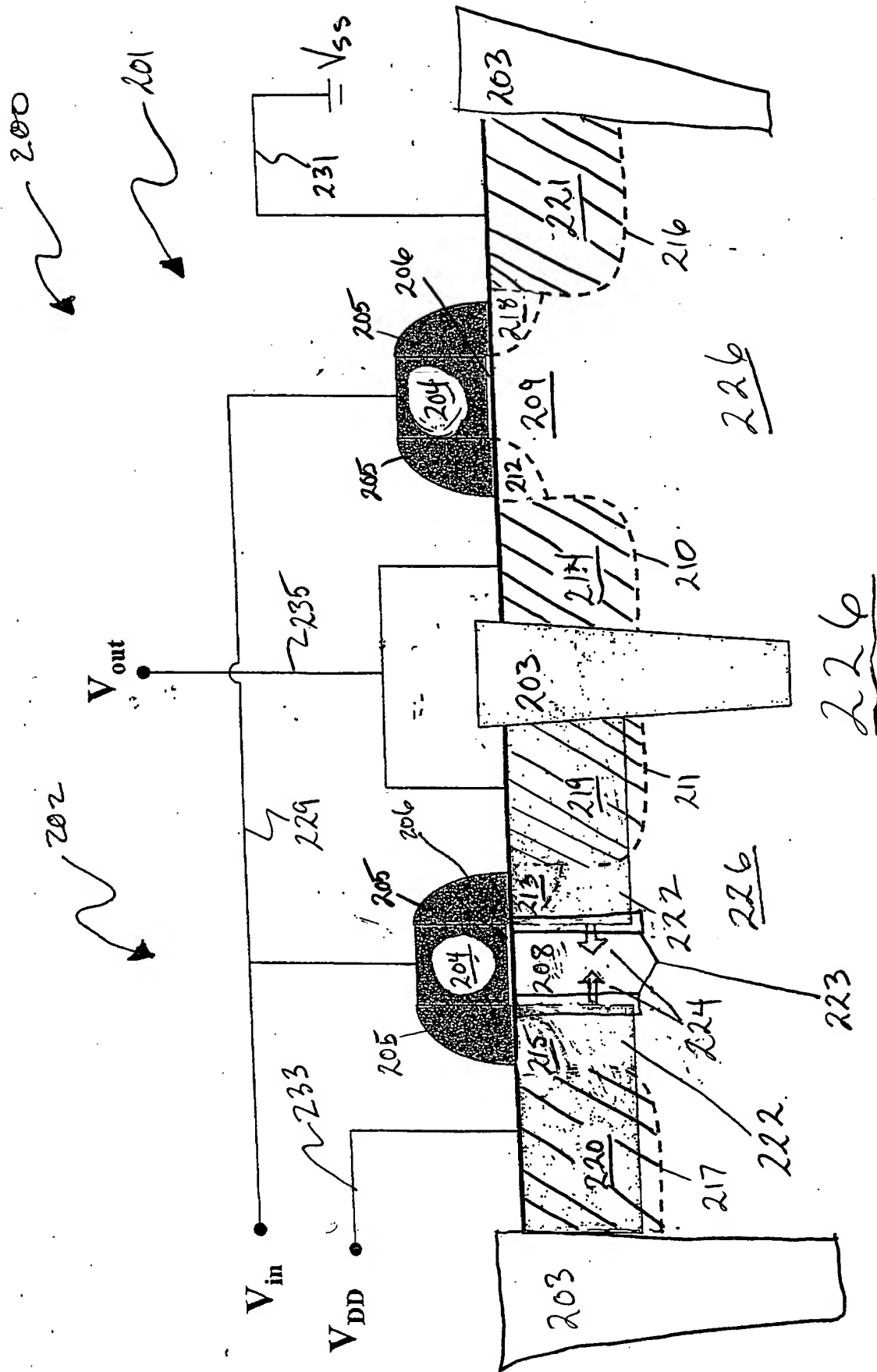


Fig 2. b)

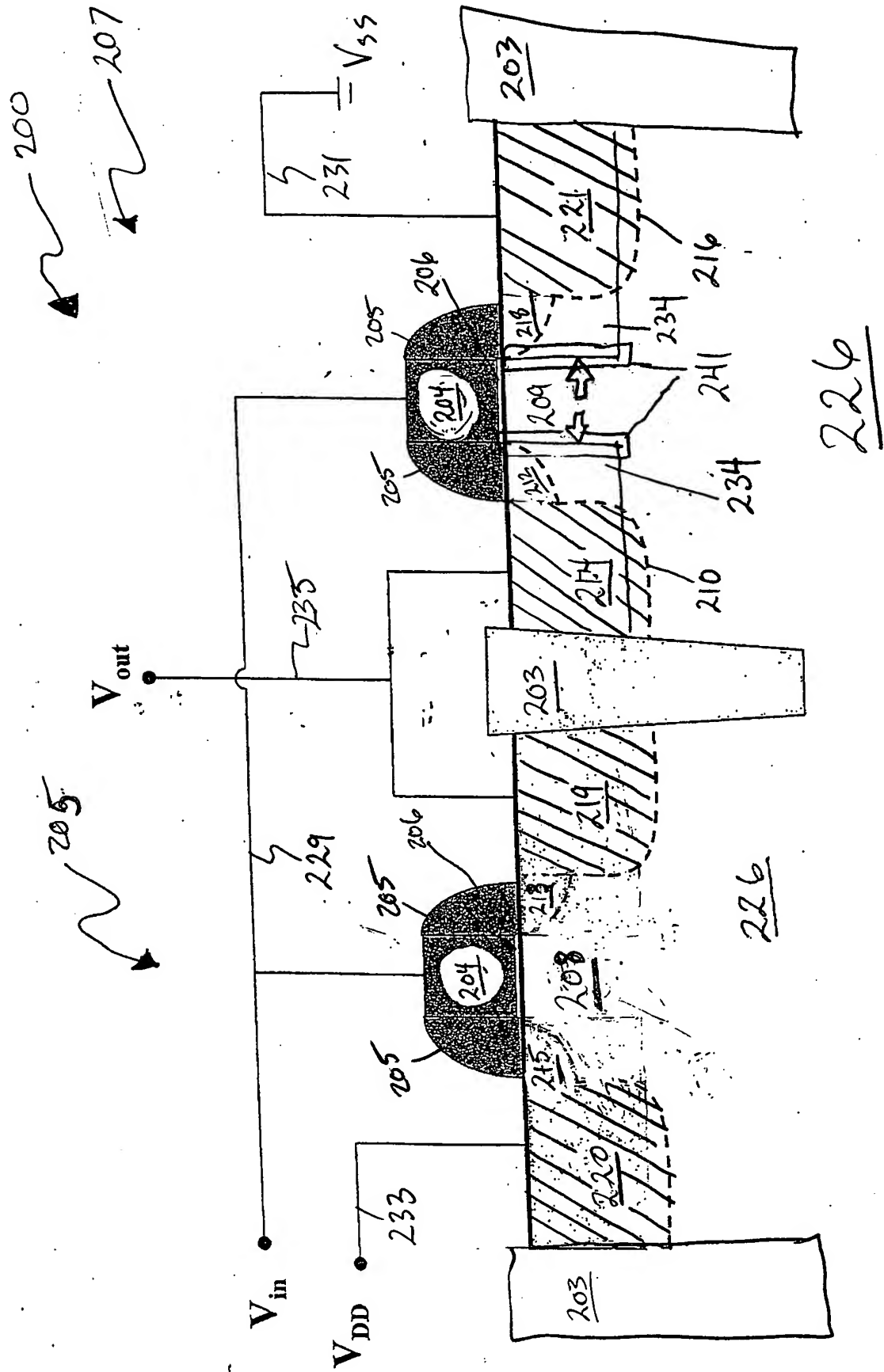


Fig 2. c)

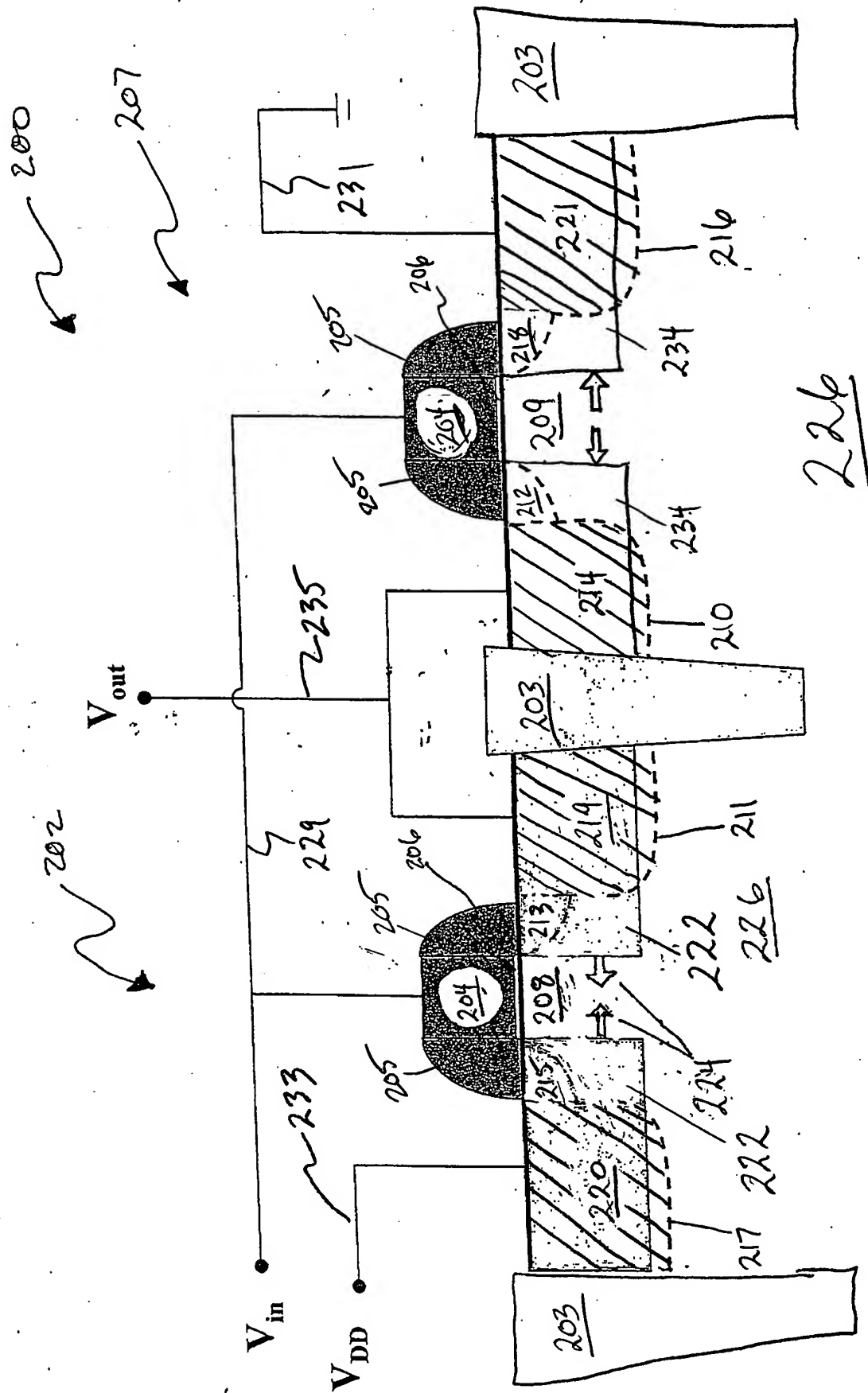


Fig. 3a)

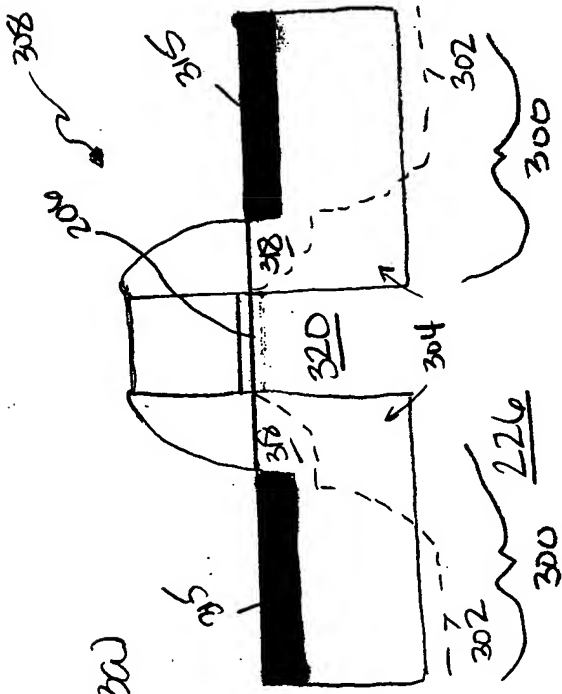


Fig. 3b)

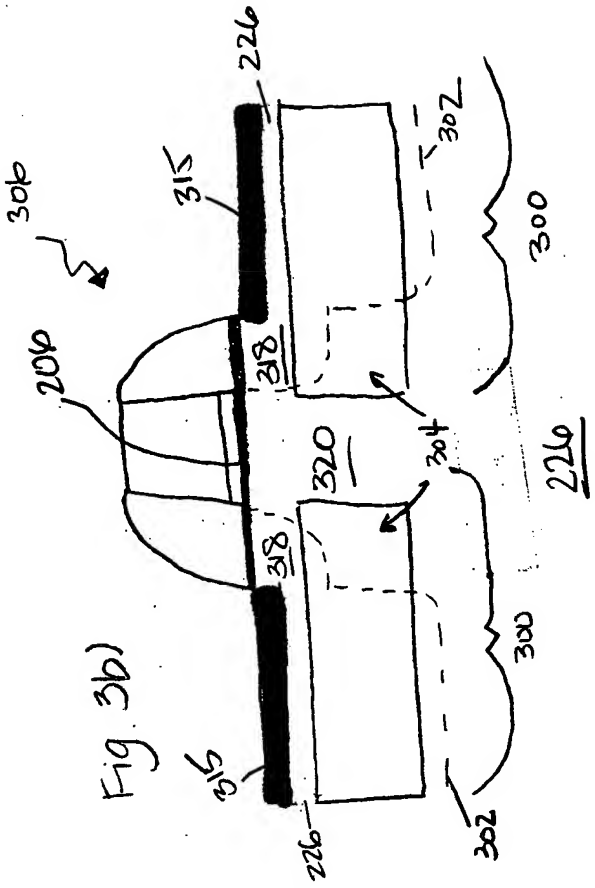


Fig. 3c)

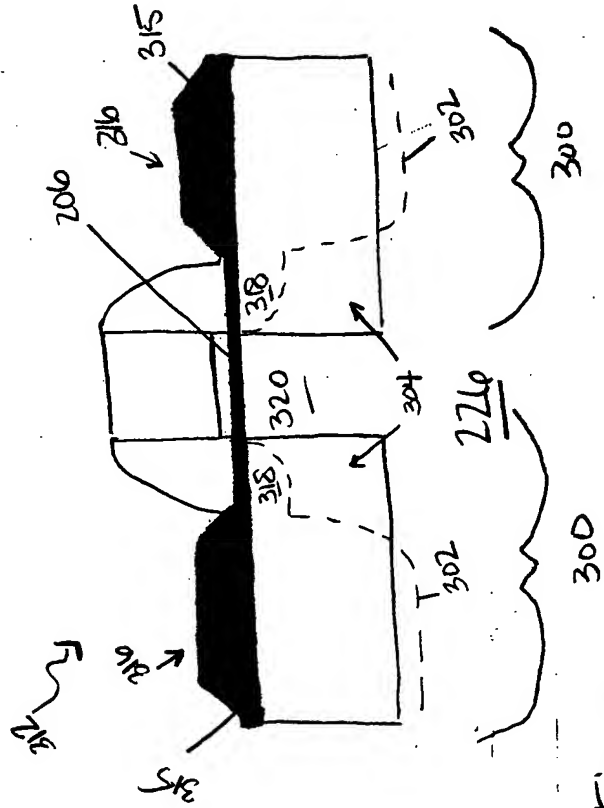


Fig. 3d)

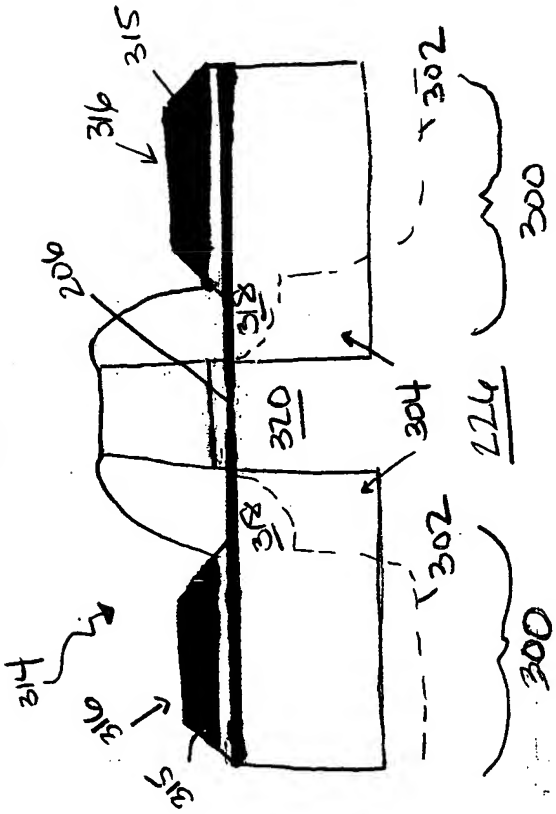


Fig 4.

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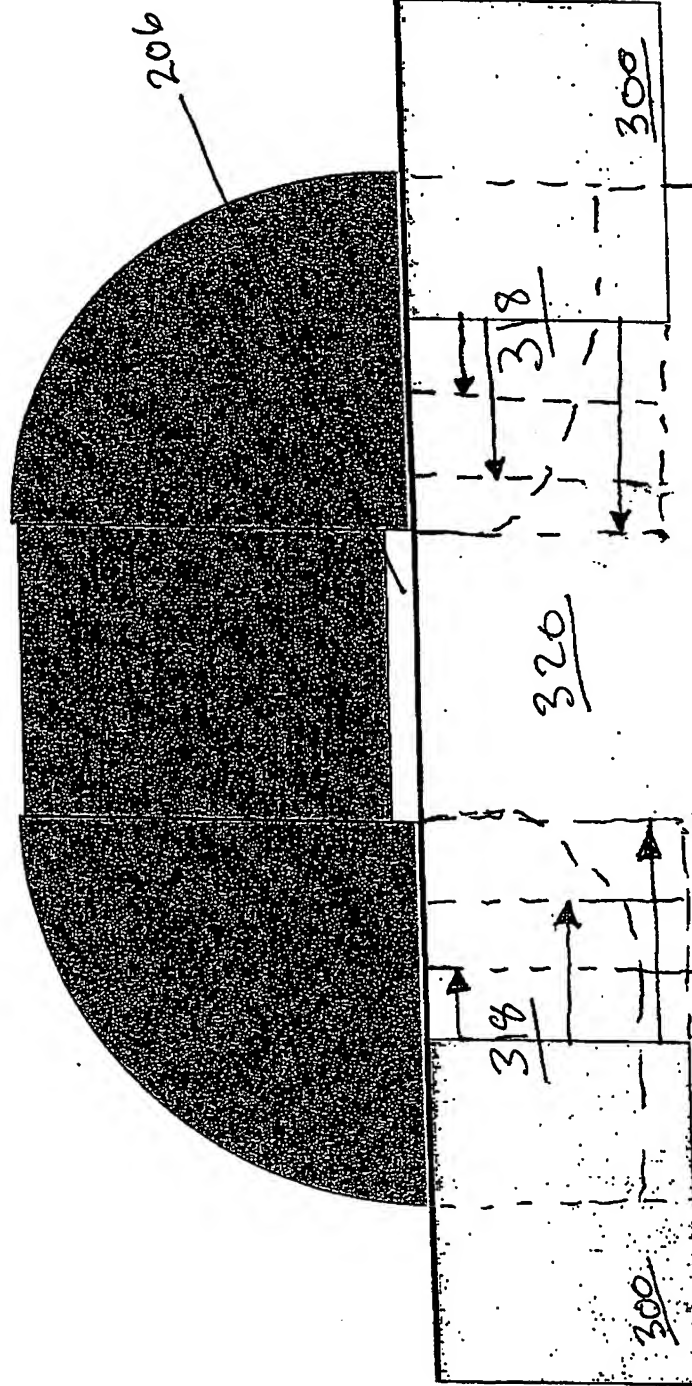




Fig. 5a

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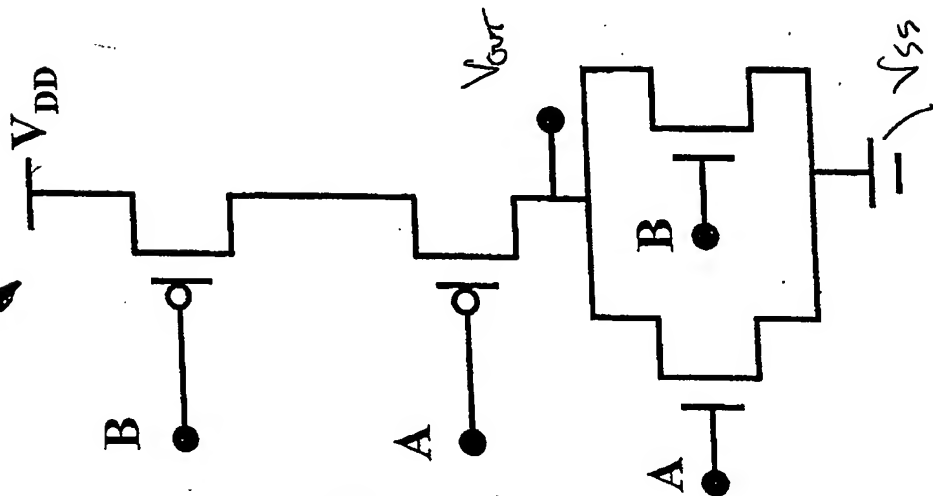


Fig 5b

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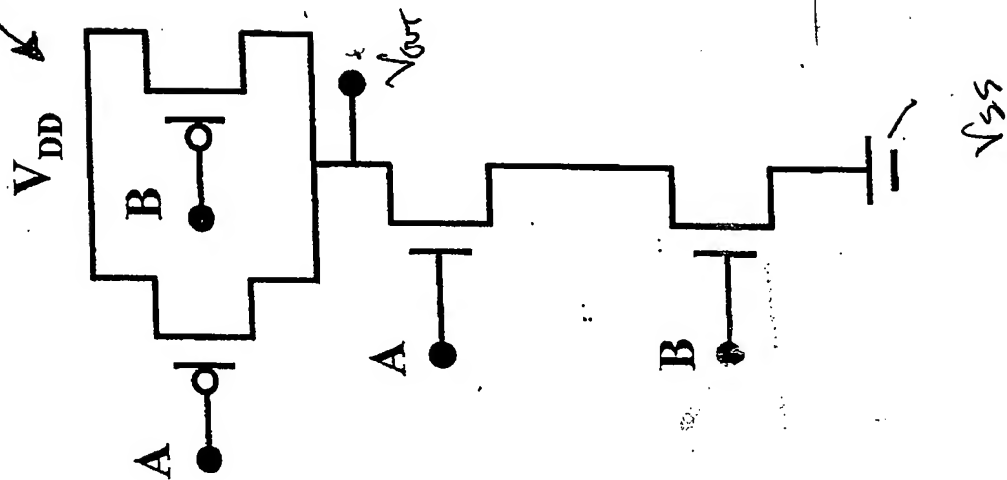
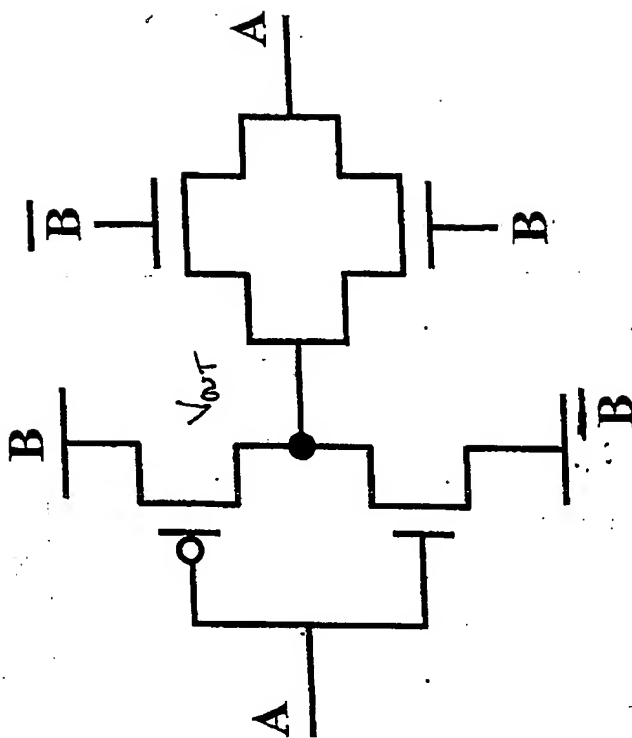


Fig. 5c

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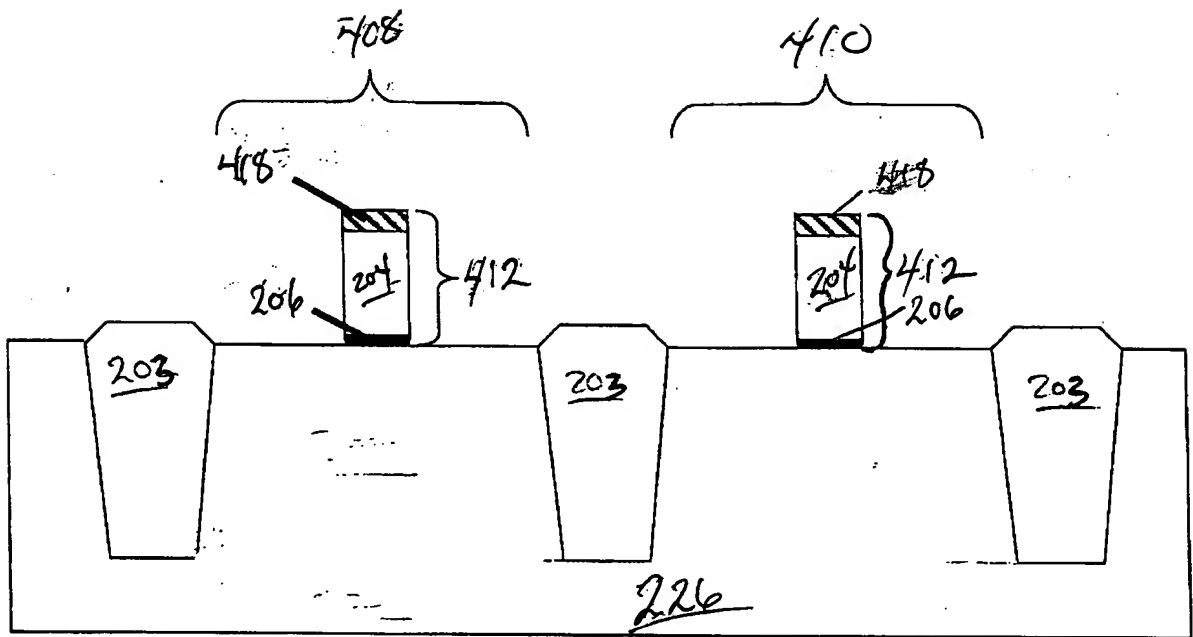


FIG. 6a

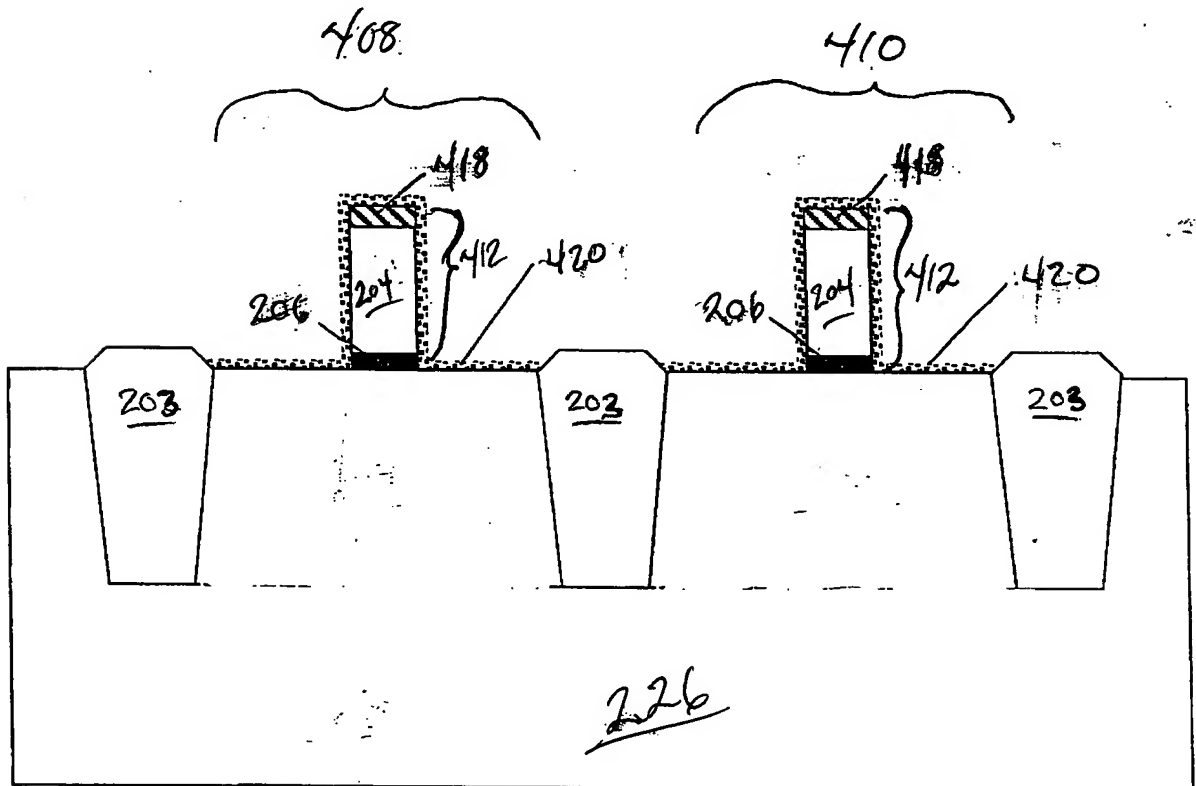


FIG. 6b

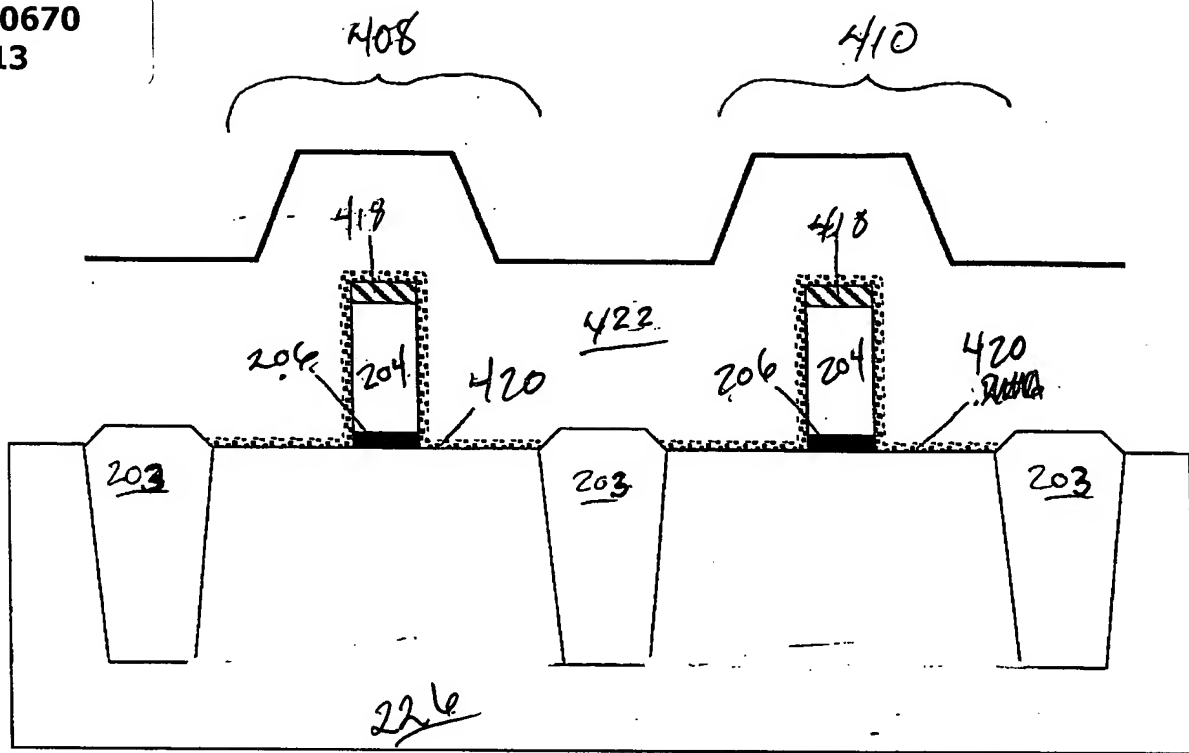


FIG. 6c

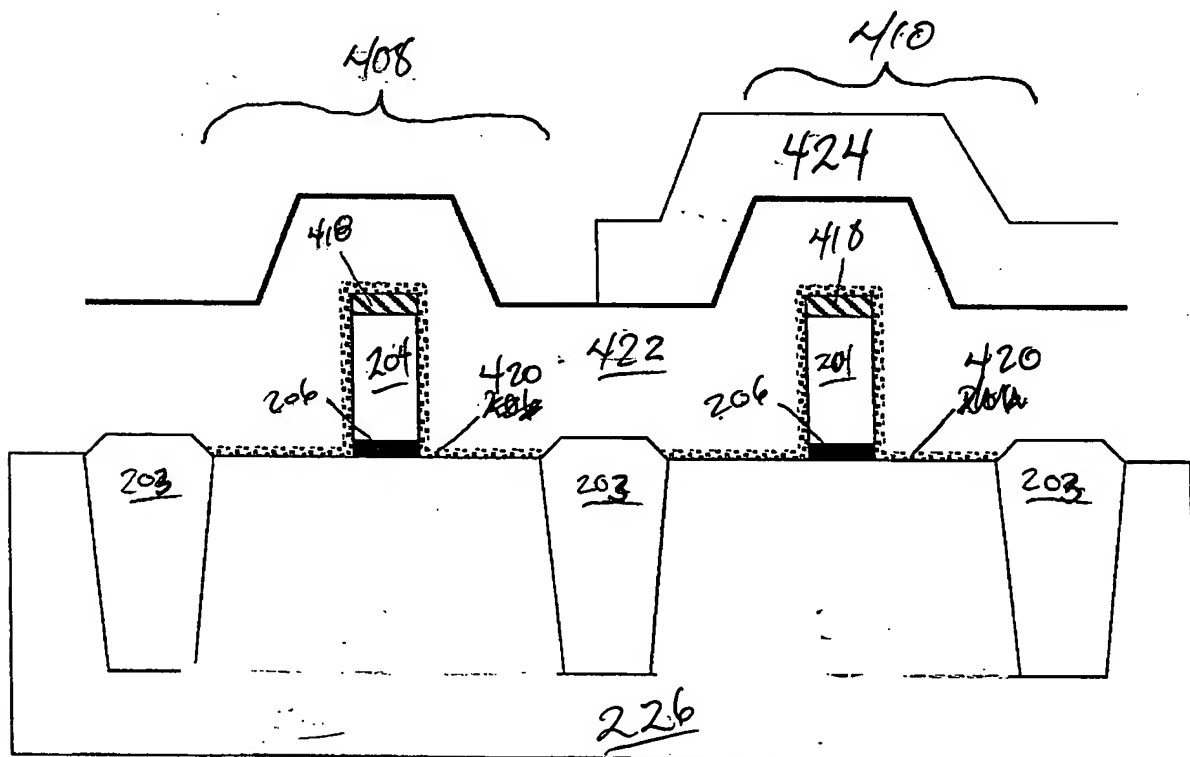


FIG. 6d

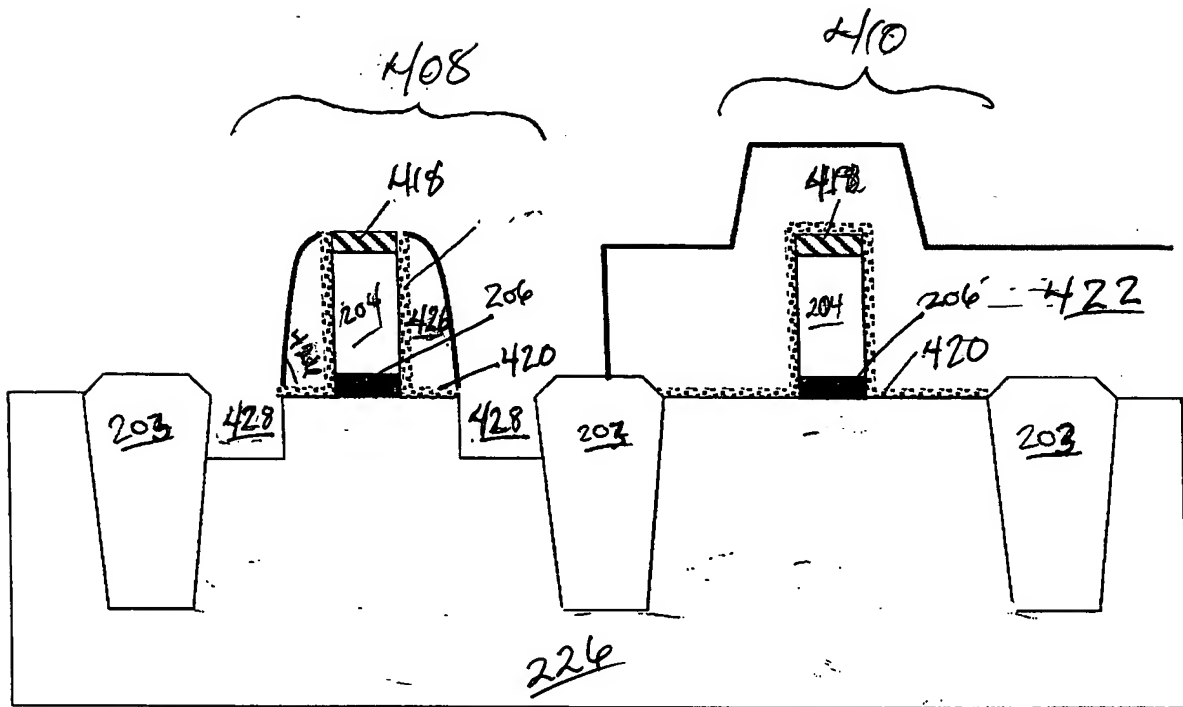


FIG. 6e

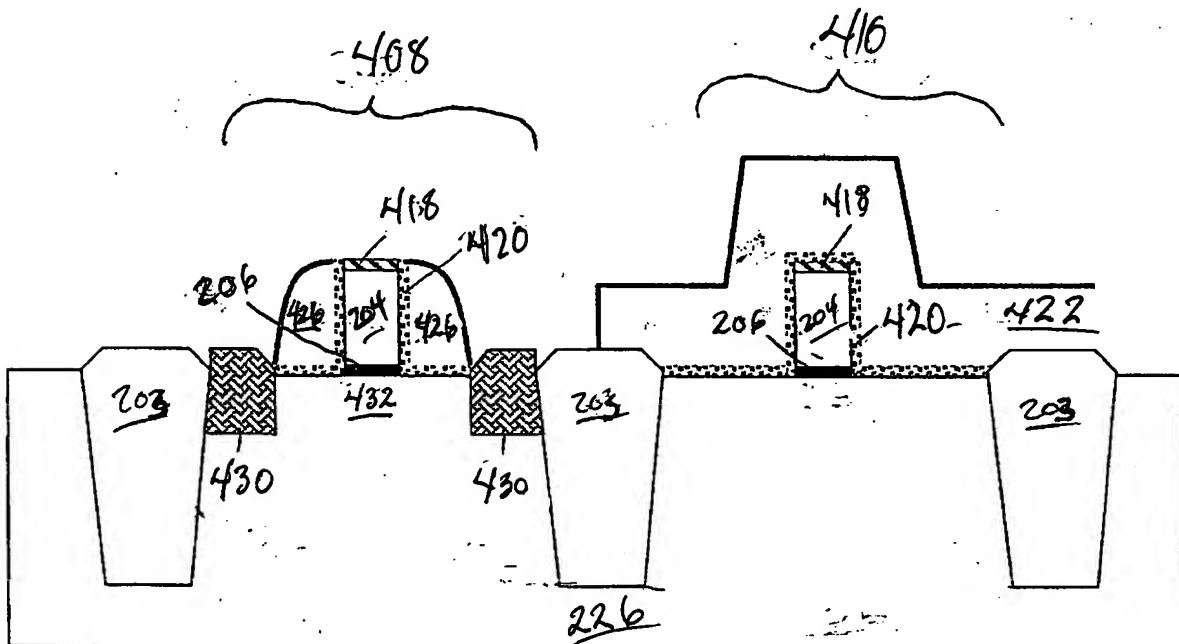


FIG. 6f

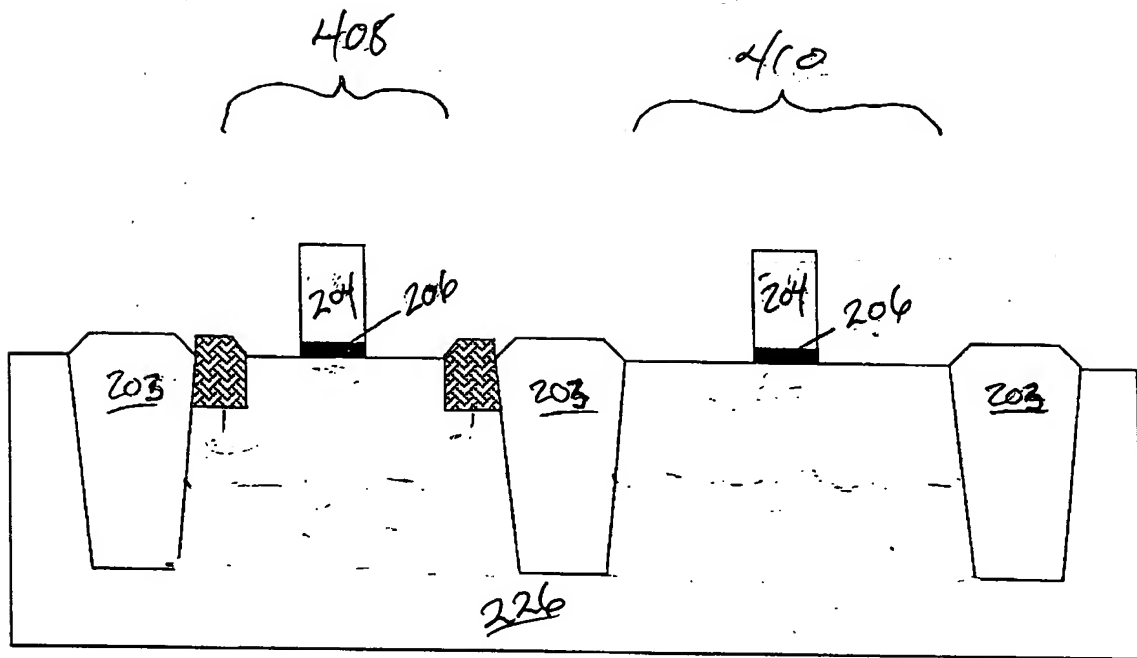


FIG. 6g

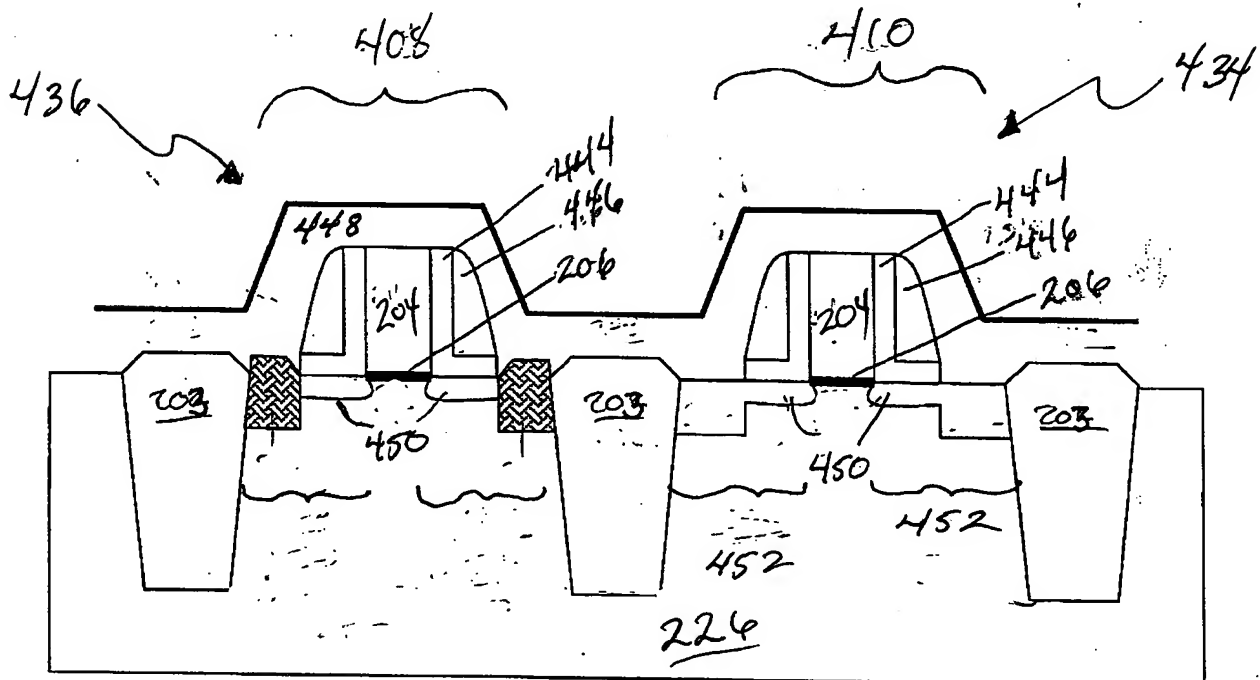


FIG. 6h



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Figure 7b

The diagram shows a common-emitter amplifier circuit. The input signal  $v_{in}$  is applied to the base of an NPN BJT through a coupling capacitor. The emitter is connected to ground ( $V_{EE}$ ) via an emitter resistor. The collector is connected to a positive supply voltage ( $V_{CC}$ ) through a load resistor. The output voltage  $v_o$  is taken from the collector terminal.